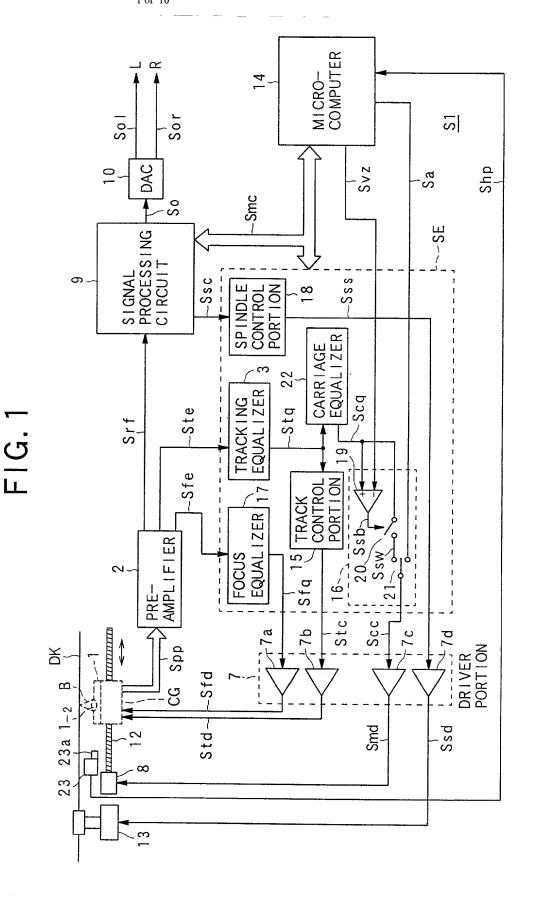
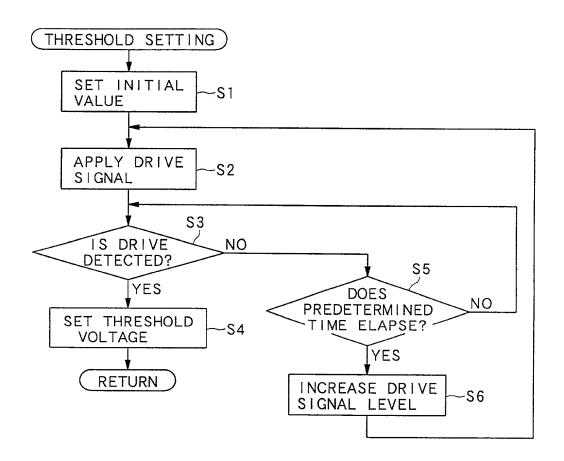
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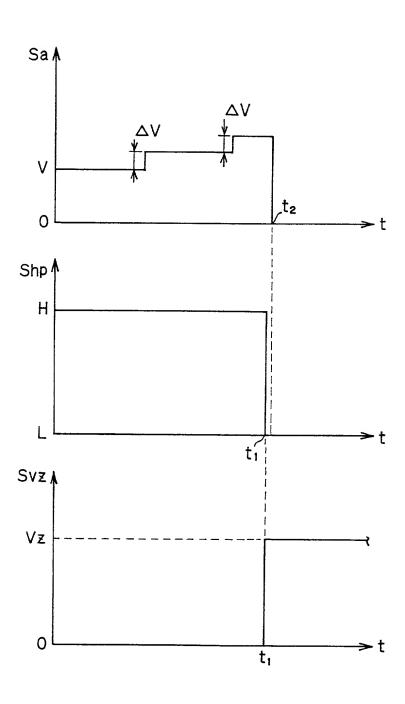
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FIG. 2



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FIG. 3



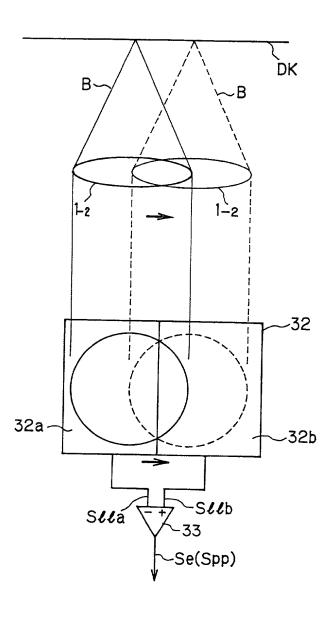
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MICRO-COMPUTER _ ≃ \$2 Sor Sol Svz DAC Sa_ -Smc So 31 SIGNAL PROCESSING -CIRCUIT SE **o** SQUARE AVERAGING PORTION -Ssc SSS SPINDLE CONTROL PORTION ∞ CARRIAGE EQUALIZER Sbp 22 BPF TRACKI NG EQUAL I ZER Scq Se Stq Ste Srf 30~ တ Sfe TRACK CONTROL PORT I ON EQUAL I ZER AMPL I F I ER Ssw **FOCUS** ري Sfq Stc Scc Spp DRIVER PORTION 첫 -Sfd ဗ္ဗ $\mathbf{\omega}$ 1-2 Std~ Smd Ssd

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FIG. 5

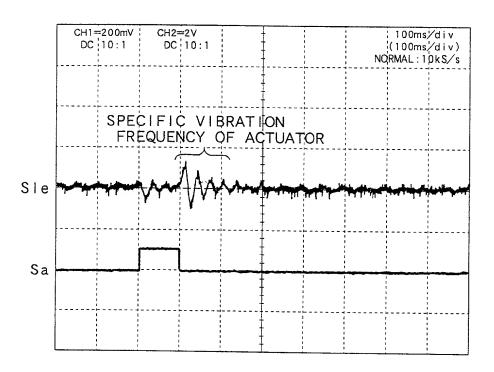


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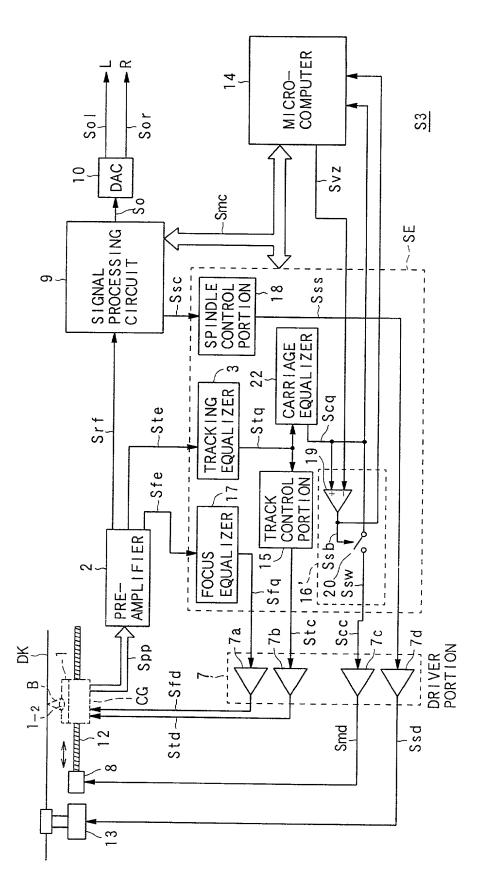
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FIG. 6



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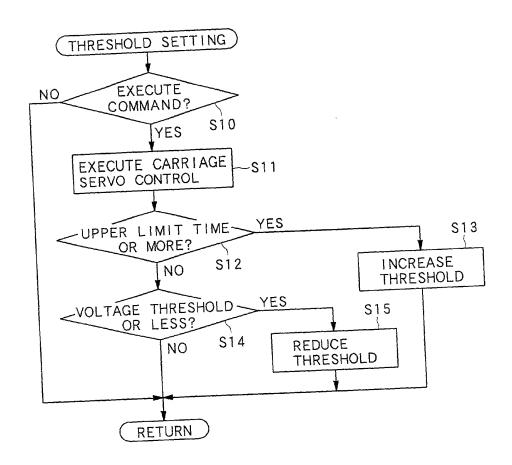
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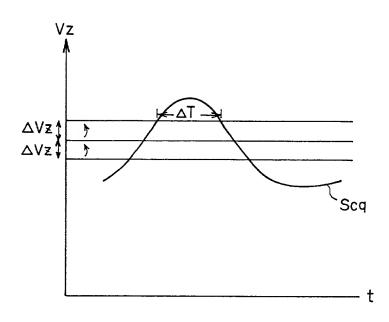
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FIG. 8



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FIG. 9



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FIG.10(a)

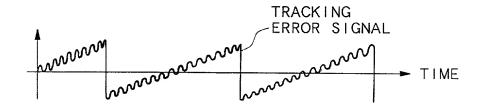


FIG.10(b)

